

VALERIU BEIU (S'92–M'95–SM'96) received the MSc in Computer Engineering from the University “Politehnica” Bucharest in 1980, and the PhD *summa cum laude* in Electrical Engineering from the Katholieke Universiteit Leuven in 1994.

Since graduating in 1980 he has been with the Research Institute for Computer Techniques, University “Politehnica” Bucharest, Katholieke Universiteit Leuven, King’s College London, Los Alamos National Laboratory, Rose Research, Washington State University, United Arab Emirates University, and currently is with “Aurel Vlaicu” University of Arad. His research interests have constantly been on biological-/neural-inspired circuits and brain-inspired architectures (low-power, highly reliable, massively parallel), being funded at over US\$ 40M, and publishing over 250 papers (over 40 invited and more than 10 patents) as well as giving over 190 invited talks, organizing over 100 conferences and working (unfortunately very slowly) on 2 books: *Emerging Brain-Inspired Nano-Architectures* and *VLSI Complexity of Discrete Neural Networks*.

Dr. Beiu has received 5 fellowships and 7 best paper awards, and is a senior member of the IEEE as well as a member of: ACM, INNS, ENNS, and MCFA. He was a member of the SRC-NNI Working Group on Novel Nano-architectures, the IEEE CS Task Force on Nano-architectures, and the IEEE Emerging Technologies Group on Nanoscale Communications, and has been an Associate Editor of the *IEEE Transactions on Neural Networks* (2005–2008) and of the *IEEE Transactions for Very Large Scale Integration Systems* (2011–2015), while being an Associate Editor of the *Nano Communication Networks* (since 2010).